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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/696,705	10/29/2003	Stephan Bolz	071308.0473	7534

31625 7590 11/01/2006

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EXAMINER

CAVALLARI, DANIEL J

ART UNIT PAPER NUMBER

2836

DATE MAILED: 11/01/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b> 10/696,705	<b>Applicant(s)</b> BOLZ, STEPHAN	
	<b>Examiner</b> Daniel J. Cavallari	<b>Art Unit</b> 2836	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

#### Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) ☒ Responsive to communication(s) filed on 20 July 2006.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) ☒ Claim(s) 1-7 and 10-20 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 1-7 and 10-17 is/are allowed.
- 6) ☒ Claim(s) 18 is/are rejected.
- 7) ☒ Claim(s) 19 and 20 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 20 July 2006 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \*    c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)                                | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                       | 5) <input type="checkbox"/> Notice of Informal Patent Application                       |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

### **DETAILED ACTION**

The examiner acknowledges a submission of the amendment filed on 7/20/2006. The amendments to the specification, drawings, claims 1-7, 10, 11, 13, 15, & 16 and cancellation of claims 8 & 9 are accepted.

### ***Response to Arguments***

1. Applicant's arguments, filed 7/20/2006, with respect to the 112 first paragraph rejection of claims 1, 4, 6, 13, 14, 18, & 20, have been fully considered and are persuasive. The 112 first paragraph rejection of claims 1, 4, 6, 13, 14, 8, & 20 have been withdrawn.

2. Applicant's arguments, filed 7/20/2006, with respect to the 112 second paragraph rejection of claims 1 & 2 have been fully considered and are persuasive. The 112 second paragraph rejection of claims 1 & 2 have been withdrawn.

### ***Drawings***

The previously made objection to the drawings has been withdrawn in view of the replacement drawings received on 7/20/2006. These new drawings are accepted.

***Claim Objections***

Claim 18 is objected to because of the following informalities:

- Claim 18 recites "...the output voltage" however "an output voltage" is not previously disclosed. There is a lack of antecedent basis for this claim.

The claim will be examined as best understood to mean "an output voltage".

Appropriate correction is required.

***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claim 18 is rejected under 35 U.S.C. 102(b) as being anticipated by McCoy et al. (US 6,516,658).

McCoy et al. (hereinafter referred to as McCoy) teaches:

- Assigning each of a plurality of injection valves a calibration resistor for which the resistance value classifies the injection valve with regard to at least one characteristic (See Column 1, Lines 35-47).
- Switching each calibration resistor individually into a calibration network which is suitable for creation of an electrical calibration voltage dependant

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on the value of the calibration resistor (See Column 4, Line 55 to Column 5, Line 42).

- Tapping an output voltage (46) of the calibration network (See Column 5, Line 35-42 & Figures 5, 6A & 6B).

***Allowable Subject Matter***

Claims 1-7 and 10-17 are allowed. Claims 19 & 20 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter:

In regard to Claims 1, 14, & 19

McCoy et al. (US 6,516,658) teaches calibrating a plurality of controllable components using resistor values which correspond to at least one characteristic (See McCoy et al. Column 1, Lines 35-67). Vener (US 4,972,293) teaches a resistor network used with controllable components using a resistor value (See Column 3, Lines 13-62) as well as a constant current source (50) (See Column 4, Line 39 to Column 5, Line 4). However prior art fails to teach the classification circuit comprising a constant current source in parallel with the reference resistor and wherein the switching means which switches each calibration resistor in and

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out of the calibration network is also connected in parallel with the reference resistor and constant current source.

### ***Conclusion***

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

- Aguayo et al. (US 5,282,943) teaches an apparatus for identification of different components.
- Corley et al. (US 4,928,002) teaches a pick and place identification apparatus.
- Miller (US 2003/0105566 A1) teaches identification of components using a resistor.
- Sakurai et al. (US 6,569,109) teaches a apparatus incorporating a current source and reading an identification resistor value (See Column 24, Lines 10-42).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Daniel J. Cavallari whose telephone number is (571)272-8541. The examiner can normally be reached on Monday-Friday 8:30-5:00.


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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Brian Sircus can be reached on (571)272-2800 x36. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Daniel Cavallari

October 4, 2006



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